

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/711,982	10/18/2004	Juergen Pille	DE920030003US1	DE920030003US1 5981	
24241	7590 06/30/2006		EXAMINER		
IBM MICROELECTRONICS			LE, THONG QUOC		
INTELLECTUAL PROPERTY LAW 1000 RIVER STREET 972 E			ART UNIT	PAPER NUMBER	
			2827		
ESSEX JUNC	TION, VT 05452		DATE MAII FD: 06/30/200	DATE MAILED: 06/30/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	W			
Office Action Summary		10/711,982	PILLE ET AL.				
		Examiner	Art Unit				
		Thong Q. Le	2827				
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the	correspondence addre	!SS			
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DAISSION of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. Depriod for reply is specified above, the maximum statutory period we tree to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATIO 36(a). In no event, however, may a reply be till apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE.	N. mely filed n the mailing date of this comm ED (35 U.S.C. § 133).				
Status							
1)🖂	Responsive to communication(s) filed on 10 Ag	oril 2006.					
2a) <u></u> □	This action is FINAL . 2b)⊠ This action is non-final.						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposit	ion of Claims						
4)🛛	Claim(s) 1-7 and 9-12 is/are pending in the app	plication.					
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠	Claim(s) <u>9-12</u> is/are allowed.						
· ·	Claim(s) <u>1-7</u> is/are rejected.						
-	Claim(s) is/are objected to.						
8)[_]	Claim(s) are subject to restriction and/or	r election requirement.					
Applicat	ion Papers						
9)	The specification is objected to by the Examine	ır.					
10)	The drawing(s) filed on is/are: a) acce	epted or b) ☐ objected to by the	Examiner.				
	Applicant may not request that any objection to the	• • • • • • • • • • • • • • • • • • • •					
44)	Replacement drawing sheet(s) including the correct						
11)[_]	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-	152.			
Priority (under 35 U.S.C. § 119						
,	Acknowledgment is made of a claim for foreign All b) Some * c) None of:	priority under 35 U.S.C. § 119(a	ı)-(d) or (f).				
,	1. Certified copies of the priority documents	s have been received.					
	2. Certified copies of the priority documents	s have been received in Applicat	tion No				
	3. Copies of the certified copies of the prior	rity documents have been receiv	ed in this National Sta	age .			
	application from the International Bureau	• • • • • • • • • • • • • • • • • • • •					
* (See the attached detailed Office action for a list	of the certified copies not receiv	ed.				
Attachmer	nt(s)						
1) Notic	ce of References Cited (PTO-892)	4) Interview Summar					
	ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail D 5) Notice of Informal	Date Patent Application (PTO-15	52)			
	er No(s)/Mail Date	6) Other:					

Application/Control Number: 10/711,982 Page 2

Art Unit: 2827

DETAILED ACTION

1. Amendment filed on 04/01/2006 has been entered.

2. Claims 1-7,9-12 are presented for examination.

Response to Arguments

3. Applicant's arguments with respect to claims 1-7,9-12 have been considered but are most in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 5. Claims 1-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Pang et al. (U.S. Patent No. 6,373,779).

Regarding claim 1, Pang et al. disclose a method for accessing memory cells (Figure 3) within a memory array operated with a precharge mechanism, in which differential read and write access operations are performed by activating a true bitline and a complement bitline (Figure 5, Column 5, lines 30-67, column 6, lines 1-4), the method comprising:

determining whether a next memory access operation (Column 6, lines 43-45) occurring subsequent to a current access operation is a read access operation or a write access operation (Column 5, lines 38-46, current access is a read access); and

Application/Control Number: 10/711,982

Art Unit: 2827

performing a precharge of the true and complement bitlines only when a read access operation follows the current access operation (Column 5, lines 30-38, lines 60-67, Column 6, lines 17-25).

Regarding claim 2, Pang et al. disclose wherein the memory array comprises a SRAM array (Column 2, lines 50).

Regarding claim 3, Pang et al. disclose in which a first precharge control signal is combined with read cycle (n+1) control signal evaluate whether a next memory access cycle comprises a read access or write access (Column 6, lines 6, lines 38-50).

Regarding claim 4, Pang et al. disclose wherein the first precharge control signal and read cycle n+1 control signal are combined to yield a second precharge signal (Column 7, lines 13-35).

Regarding claim 5, Pang et al. disclose wherein the read cycle (n+1) control signal is assert according to an operating mode of the memory array, such that a write access operation occurring over a plurality of system clock cycles results in a continuous assertion of the next read cycle (n+1) control signal until the write access operation is complete (Column 6, lines 38-50).

Regarding claim 6, Pang et al. disclose wherein the read cycle (n+1) control signal is asserted two system clock cycles in advance of a next access operation during a delay between when an address of the memory array is specified and a current access operation is completed (Column 6, lines 5-50).

Regarding claim 7, Pang et al. disclose wherein the next read cycle (n+1) control signal is assert after a delay of one clock cycle during a period of time when no memory operation is performed (Column 6, lines 27-50).

Allowable Subject Matter

6. Claims 9-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 9-12 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Pang et al. (U.S. Patent No. 6,373,779), and others, does not teach the claimed invention having a an integrated circuit memory array adapted for low operation including a logic AND gate or a multiplexer adapted to evaluate the first precharge control signal and read cycle control (n+1) signal the logic AND gate asserting a second precharge control signal when a next memory access is a read access operation for controlling the precharge circuit.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zarabian Amir can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/711,982 Page 5

Art Unit: 2827

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Thong Q. Le Primary Examiner

Thoyle

Art Unit 2827

6/16/2006